

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L35	4	(servo) and (channel) and (programm\$4 near10 ((limit\$4 filter) same ((limit benchamrk threshold value) and (upper high\$4 big\$4) and (lower low\$4 under small\$3))))	US-PGPUB	OR	ON	2005/09/01 10:39
L34	0	(servo) and (channel) and (programm\$4 near10 ((limit\$4 filter) same ((limit benchamrk threshold value) and (upper high\$4 big\$4) and (lower low\$4 under small\$3))))	DERWENT	OR	ON	2005/09/01 10:39
L33	0	(servo) and (channel) and (programm\$4 near10 ((limit\$4 filter) same ((limit benchamrk threshold value) and (upper high\$4 big\$4) and (lower low\$4 under small\$3))))	JPO	OR	ON	2005/09/01 10:39
L32	0	(servo) and (channel) and (programm\$4 near10 ((limit\$4 filter) same ((limit benchamrk threshold value) and (upper high\$4 big\$4) and (lower low\$4 under small\$3))))	EPO	OR	ON	2005/09/01 10:39
L31	23	(servo) and (channel) and (programm\$4 near10 ((limit\$4 filter) same ((limit benchamrk threshold value) and (upper high\$4 big\$4) and (lower low\$4 under small\$3))))	USPAT	OR	ON	2005/09/01 10:39
L29	0	(servo) and (channel) and (programmable near5 ((limit\$4 filter) near3 ((limit benchamrk threshold value) and (upper high\$4 big\$4) and (lower low\$4 under small\$3))))	USPAT	OR	ON	2005/09/01 10:38
S38	110897	(limit\$4 threshold benchmark) near5 ((upper high\$4) and (lower low\$4))	USPAT	OR	ON	2005/09/01 10:36
L23	4	(servo) and (channel) and (programmable adj3 ((limit\$4 filter) near3 ((upper high\$4 big\$4) and (lower low\$4 under small\$3))))	USPAT	OR	ON	2005/09/01 10:36
L28	0	(servo) and (channel) and (programmable adj3 ((limit\$4 filter) near3 ((upper high\$4 big\$4) and (lower low\$4 under small\$3))))	IBM_TDB	OR	ON	2005/09/01 10:35
L27	0	(servo) and (channel) and (programmable adj3 ((limit\$4 filter) near3 ((upper high\$4 big\$4) and (lower low\$4 under small\$3))))	DERWENT	OR	ON	2005/09/01 10:35

L26	0	(servo) and (channel) and (programmable adj3 ((limit\$4 filter) near3 ((upper high\$4 big\$4) and (lower low\$4 under small\$3))))	JPO	OR	ON	2005/09/01 10:35
L25	0	(servo) and (channel) and (programmable adj3 ((limit\$4 filter) near3 ((upper high\$4 big\$4) and (lower low\$4 under small\$3))))	EPO	OR	ON	2005/09/01 10:35
L24	1	(servo) and (channel) and (programmable adj3 ((limit\$4 filter) near3 ((upper high\$4 big\$4) and (lower low\$4 under small\$3))))	US-PGPUB	OR	ON	2005/09/01 10:35
S42	49	programmable adj3 ((limit\$4) near3 ((upper high\$4) and (lower low\$4)))	USPAT	OR	ON	2005/09/01 10:33
L22	10	("5654765" "5835532" "6002713" "6052349" "6130793" "6141167" "6201832" "6289063" "6381271" "6505222").pn.	USPAT	OR	ON	2005/09/01 10:32
L21	5	("5552942" "5576904" "5576906" "5796535" "5805001").pn.	USPAT	OR	ON	2005/09/01 10:31
L20	4	("5341249" "5400029" "5602503" "5847891").pn.	USPAT	OR	ON	2005/09/01 10:30
L14	34	((servo near10 demodulat\$4) and (robustness enhancement better best) and (filter\$4 equaliz\$4 limit\$4)) and (((VGA (variable adj2 gain adj2 amplifier)) and (read adj5 channel) and (AGC (automatic adj2 gain adj2 control)) and (PLL (phase adj L1 lock\$3 adj2 loop))))	USPAT	OR	ON	2005/09/01 10:30
L19	0	((servo near10 demodulat\$4) and (robustness enhancement better best) and (filter\$4 equaliz\$4 limit\$4)) and (((VGA (variable adj2 gain adj2 amplifier)) and (read adj5 channel) and (AGC (automatic adj2 gain adj2 control)) and (PLL (phase adj L1 lock\$3 adj2 loop))))	IBM_TDB	OR	ON	2005/09/01 10:29
L18	1	((servo near10 demodulat\$4) and (robustness enhancement better best) and (filter\$4 equaliz\$4 limit\$4)) and (((VGA (variable adj2 gain adj2 amplifier)) and (read adj5 channel) and (AGC (automatic adj2 gain adj2 control)) and (PLL (phase adj L1 lock\$3 adj2 loop))))	DERWENT	OR	ON	2005/09/01 10:29
L17	0	((servo near10 demodulat\$4) and (robustness enhancement better best) and (filter\$4 equaliz\$4 limit\$4)) and (((VGA (variable adj2 gain adj2 amplifier)) and (read adj5 channel) and (AGC (automatic adj2 gain adj2 control)) and (PLL (phase adj L1 lock\$3 adj2 loop))))	JPO	OR	ON	2005/09/01 10:29

L16	0	((servo near10 demodulat\$4) and (robustness enhancement better best) and (filter\$4 equaliz\$4 limit\$4)) and ((VGA (variable adj2 gain adj2 amplifier)) and (read adj5 channel) and (AGC (automatic adj2 gain adj2 control)) and (PLL (phase adj L1 lock\$3 adj2 loop))))	EPO	OR	ON	2005/09/01 10:29
L15	14	((servo near10 demodulat\$4) and (robustness enhancement better best) and (filter\$4 equaliz\$4 limit\$4)) and ((VGA (variable adj2 gain adj2 amplifier)) and (read adj5 channel) and (AGC (automatic adj2 gain adj2 control)) and (PLL (phase adj L1 lock\$3 adj2 loop))))	US-PGPUB	OR	ON	2005/09/01 10:29
L9	1	(VGA (variable adj2 gain adj2 amplifier)) same (read adj5 channel) same (AGC (automatic adj2 gain adj2 control)) same (PLL (phase adj L1 lock\$3 adj2 loop)) same (servo)	US-PGPUB	OR	ON	2005/09/01 10:29
S28	34	((servo near10 demodulat\$4) and (robustness enhancement better or best) and (filter\$4 equaliz\$4 limit\$4)) and ((VGA (variable adj2 gain adj2 amplifier)) and (read adj5 channel) and (AGC (automatic adj2 gain adj2 control)) and (PLL (phase adj S2 lock\$3 adj2 loop))))	USPAT	OR	ON	2005/09/01 10:28
L13	0	(VGA (variable adj2 gain adj2 amplifier)) same (read adj5 channel) same (AGC (automatic adj2 gain adj2 control)) same (PLL (phase adj L1 lock\$3 adj2 loop)) same (servo)	IBM_TDB	OR	ON	2005/09/01 10:28
L12	0	(VGA (variable adj2 gain adj2 amplifier)) same (read adj5 channel) same (AGC (automatic adj2 gain adj2 control)) same (PLL (phase adj L1 lock\$3 adj2 loop)) same (servo)	DERWENT	OR	ON	2005/09/01 10:28
L11	0	(VGA (variable adj2 gain adj2 amplifier)) same (read adj5 channel) same (AGC (automatic adj2 gain adj2 control)) same (PLL (phase adj L1 lock\$3 adj2 loop)) same (servo)	JPO	OR	ON	2005/09/01 10:28
L10	0	(VGA (variable adj2 gain adj2 amplifier)) same (read adj5 channel) same (AGC (automatic adj2 gain adj2 control)) same (PLL (phase adj L1 lock\$3 adj2 loop)) same (servo)	EPO	OR	ON	2005/09/01 10:28

S5	1	(VGA (variable adj2 gain adj2 amplifier)) same (read adj5 channel) same (AGC (automatic adj2 gain adj2 control)) same (PLL (phase adj S2 lock\$3 adj2 loop)) same (servo)	USPAT	OR	ON	2005/09/01 10:27
L8	1	(VGA (variable adj2 gain adj2 amplifier)) same (read adj5 channel) same (AGC (automatic adj2 gain adj2 control)) same (PLL (phase adj L1 lock\$3 adj2 loop)) same (servo)	USPAT	OR	ON	2005/09/01 10:27
L7	1	((servo) and (read near5 channel) and (demodulat\$4 deserializ\$4 demultiplex\$4) and (amplitude gain) and (sum\$5) and (error) and (difference subtract\$4) and ((AGC) (amplitude adj3 gain adj3 control)) and (filter) and (limit\$4 threshold value range) and (feedback (feed near3 back)) and ((big\$4 and small\$4) (upper and lower) (high\$3 and low\$3))))).clm.	US-PGPUB	OR	ON	2005/09/01 10:27
S65	16	((servo) and (read near5 channel) same (demodulat\$4 deserializ\$4 demultiplex\$4) and (amplitude gain) and (sum\$5) and (error) and (difference subtract\$4) and ((AGC) (amplitude adj3 gain adj3 control)) and (filter) and (limit\$4 threshold value range) same (feedback (feed near3 back)) and ((big\$4 and small\$4) (upper and lower) (high\$3 and low\$3))))	USPAT	OR	ON	2005/09/01 10:26
L6	0	((VGA (variable adj2 gain adj2 amplifier)) and (read adj5 channel) near10 (AGC (automatic adj2 gain adj2 control)) near10 (PLL (phase adj L1 lock\$3 adj2 loop))) and (servo) and (limit\$3 or filter\$4)	DERWENT	OR	ON	2005/09/01 10:26
L5	0	((VGA (variable adj2 gain adj2 amplifier)) and (read adj5 channel) near10 (AGC (automatic adj2 gain adj2 control)) near10 (PLL (phase adj L1 lock\$3 adj2 loop))) and (servo) and (limit\$3 or filter\$4)	JPO	OR	ON	2005/09/01 10:26
L4	0	((VGA (variable adj2 gain adj2 amplifier)) and (read adj5 channel) near10 (AGC (automatic adj2 gain adj2 control)) near10 (PLL (phase adj L1 lock\$3 adj2 loop))) and (servo) and (limit\$3 or filter\$4)	EPO	OR	ON	2005/09/01 10:26

L3	2	((VGA (variable adj2 gain adj2 amplifier)) and (read adj5 channel) near10 (AGC (automatic adj2 gain adj2 control)) near10 (PLL (phase adj L1 lock\$3 adj2 loop))) and (servo) and (limit\$3 or filter\$4)	US-PGPUB	OR	ON	2005/09/01 10:26
L2	7	((VGA (variable adj2 gain adj2 amplifier)) and (read adj5 channel) near10 (AGC (automatic adj2 gain adj2 control)) near10 (PLL (phase adj L1 lock\$3 adj2 loop))) and (servo) and (limit\$3 or filter\$4)	USPAT	OR	ON	2005/09/01 10:26
L1	15	("5050016" "5170299" "5255136" "5384671" "5420730" "5477103" "5544135" "5796535" "5903410" "5963387" "5966258" "6021012" "6111710" "6155199" "6525615").pn.	USPAT	OR	OFF	2005/09/01 10:26
S13	33	((VGA (variable adj2 gain adj2 amplifier)) and (read adj5 channel) and (AGC (automatic adj2 gain adj2 control)) and (PLL (phase adj S2 lock\$3 adj2 loop))) and (servo) and (limit\$3 or filter\$4)	US-PGPUB	OR	ON	2005/09/01 10:25
S64	0	((servo) same (read near5 channel) same (demodulat\$4 deserializ\$4 demultiplex\$4) same (amplitude gain) same (sum\$5) same (error) same (difference subtract\$4) same ((AGC (amplitude adj3 gain adj3 control)) same (filter) same (limit\$4 threshold value range) same (feedback (feed near3 back)) same ((big\$4 and small\$4) (upper and lower) (high\$3 and low\$3)))	USPAT	OR	ON	2005/08/31 10:47
S62	2	("6483789" "6754019").pn.	USPAT	OR	OFF	2005/08/31 10:21